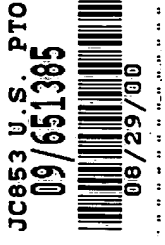


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Application of:

Sanjay Dabral and Krishna Seshan

Serial No.

Filed: August 29, 2000

For: *DIODE AND TRANSISTOR DESIGN FOR
HIGH SPEED I/O*

Divisional Application of:

Serial No. 09/107,351

Filed: June 30, 1998

Examiner: Ortiz, E.

Art Unit 2815

INFORMATION DISCLOSURE STATEMENT

In accordance with 37 C.F.R. § 1.56, Applicants hereby disclose the prior art references that have been submitted and/or cited in related application, as follows:

U.S. Serial No. 09/107,351 filed June 30, 1998.

Copies of Forms 1449 and/or 892 of this prior related application are enclosed herewith.

Copies of the references are not provided as permitted by 37 C.F.R. § 1.98(d).

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN, LLP

Date: 8/29/00

William T. Babbitt
William Thomas Babbitt, Reg. No. 39,591

12400 Wilshire Blvd., 7th Floor
Los Angeles, California 90025
(310) 207-3800